



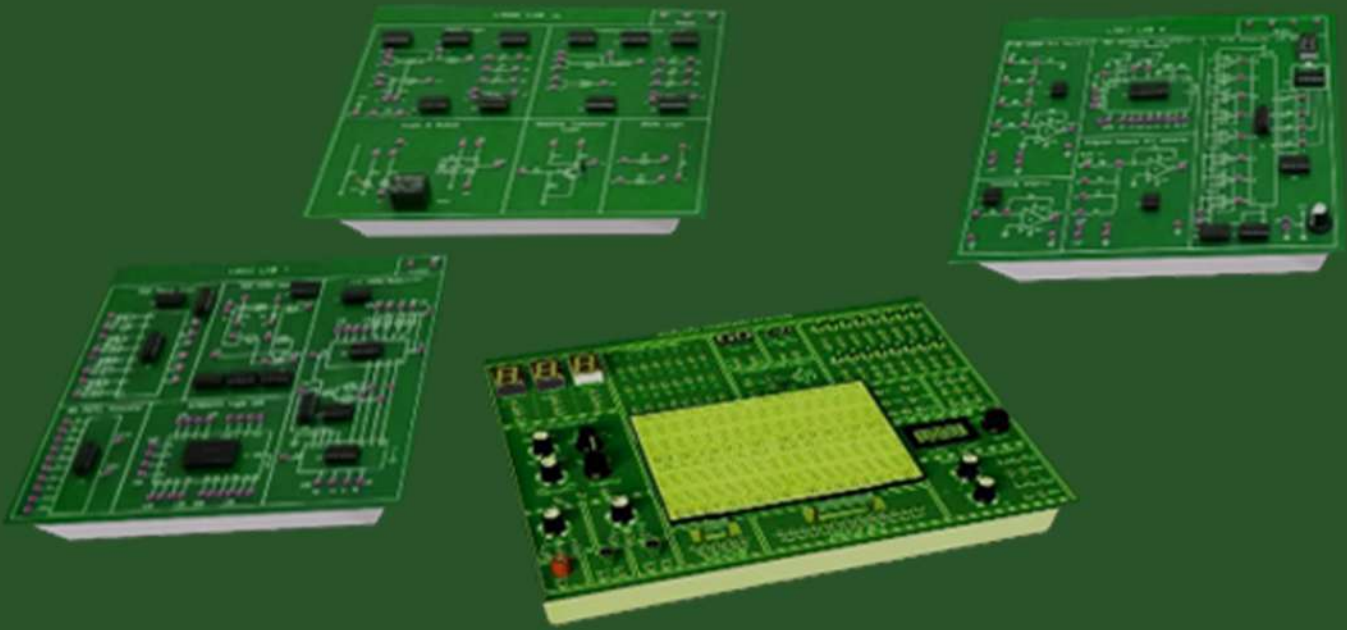
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Special Package for Analog Lab Trainer Units are

- Main Lab Module
- 3 Sets of Lab Modules



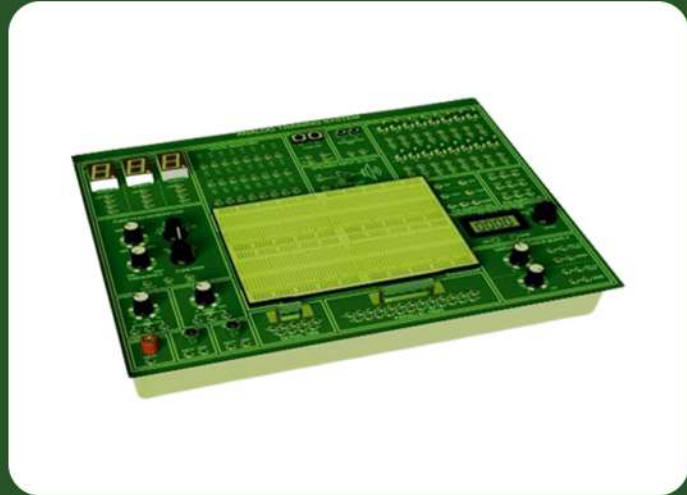
Model No. : GP-AN22



Electrical and Electronic Lab Main Module

Technical Features

- Fixed DC Power Supply: +5V, -5V, +12V, -12V
- Variable DC Power Supply: 0 ~ +25V
- Variable DC Power Supply: 0 ~ -25
- Fixed AC Supply: 2V-0-2V, 12V-0-12V, 15V-0-15V
- Function Generator
 - Output: Sine, Square, Triangle and TTL
 - Frequency: up to 100kHz
- 3 ½-Digit Digital Voltmeter
- Logic Indicators: 24 Independent LEDs with driver interface to indicate Logic 'LO' & 'HI'
- Data Switches: 2 X 8-bit DIP Switches
- 7-Segment Display: x3 Displays with BCD to 7-Segment Decoder/Driver
- Speaker: 8 Ohm 0.5W speaker with Audio Amplifier



Experiment

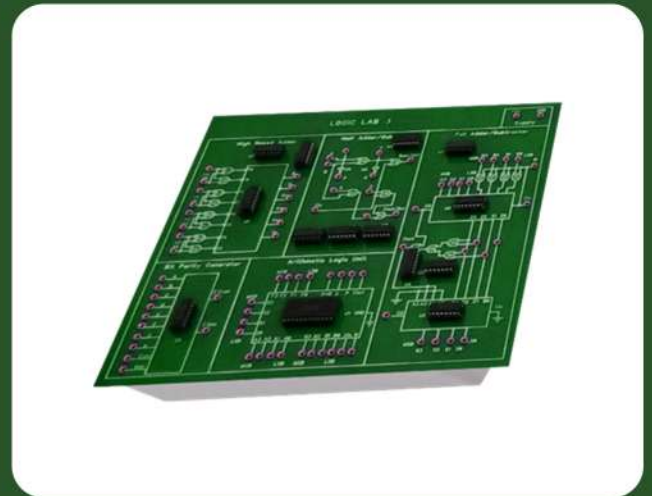
- Application of Ohm's Law
- Series/Parallel DC circuit
- Half Wave/Full Wave Rectifier Circuit
- Bridge Rectifier Circuit
- Voltage Doubler Circuit
- Characteristics of LED/BJT/FET/UJT/SCR/DIAC/TRIAC
- Non-Inverting/Inverting Amplifier Circuit
- Low/High/Band Pass Filter Circuit
- Logic Gates Operation
- Adder Operation
- 7- Segment Display Operation
- Flip flop operation



Electrical and Electronic Lab Lab Unit 3

Experiment

- Constructing Half Adder with Basic Logic Gates
- Full-Adder Circuit with IC
- High-Speed Adder Carry Generator circuit
- BCD Code Adder Circuit
- Subtractor Circuit Constructed with Basic Logic Gates
- Full-Adder and Inverter Circuit
- Arithmetic Logic Unit (ALU) Circuit
- Bit Parity Generator Circuit
- Bit Parity Generator constructed With XOR Gate
- Bit Parity Generator IC



Required Unit:

- Main Module



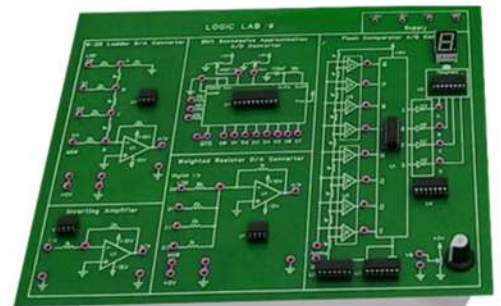


Electrical and Electronic Lab

Lab Unit 9

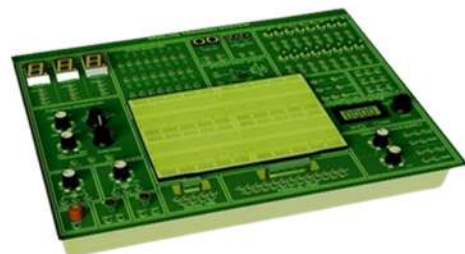
Experiment

- R -2R Ladder Network
- Inverting Amplifier Using Op-Amp
- Digital/Analog Converter (DAC) Weighted Resistor
- Analog/digital Converter (ADC) 8 Bit Successive Approximation
- Flash Comparator Circuit with 7 Segment LED Digit Display
- Inverting circuit using IC



Required Unit:

- Main Module



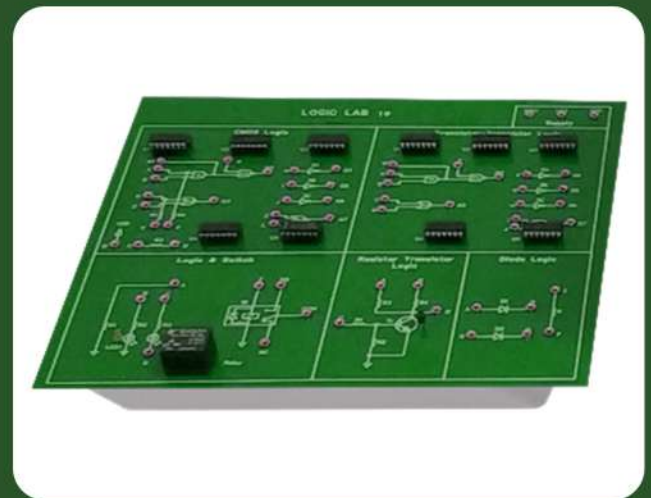


Electrical and Electronic Lab

Lab Unit 10

Experiment

- Relay Circuits
- Logic Gates Circuits
- Diode Logic (DL) Circuit
- Resistor-Transistor (RTL) Circuit
- Diode-Transistor (DTL) Circuit
- Transistor-Transistor Logic (TTL) Circuit
- Complementary-Metal Oxide-Semiconductor (CMOS) Circuit
- TTL I/O Voltage and Current Measurement
- CMOS Voltage and Current Measurement
- TTL Gate Delay time Measurement
- CMOS Gate Delay Time Measurement
- AND Gate Characteristics Measurement
- OR Gate Characteristics



Required Unit:

- Main Module

